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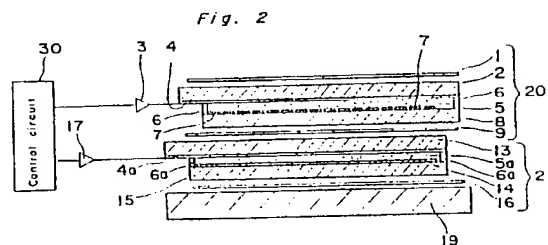
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(54) Liquid crystal display device.

(57) A liquid crystal display device comprising a liquid crystal display panel for displaying picture elements in accordance with a predetermined voltage applied thereto is disclosed. In the liquid crystal display device, a light shutter is arranged between the liquid crystal display panel and a light source, and the light shutter shades the light incident to the picture elements being rewritten from the light source.



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Description

Liquid crystal display device

BACKGROUND OF THE INVENTION

FIELD OF THE INVENTION

The present invention relates to a liquid crystal display device, and more particularly, to a light transmissive type ferroelectric liquid crystal display device having a matrix type display panel.

DESCRIPTION OF PRIOR ART

Figs. 7 and 8 show fundamental structures of a conventional matrix type display with use of a light transmissive type ferroelectric liquid crystal display.

In Figs. 7 and 8, polarizers 1 and 9 arranged to have crossed Nicols relation are formed oppositely to each other, and a light source (not shown) is arranged on the outer surface of the polarizer 9. Transparent substrates 2 and 8 of glass are arranged on the inner surfaces of the polarizers 1 and 9, respectively. Sixteen transparent conductive scanning electrodes 4 (L_1 to L_{16}) having a strip shape are formed in parallel with each other on the inner surface of the transparent substrate 2, and sixteen transparent conductive signal electrodes 7 for applying a signal having a strip shape are formed in parallel with each other on the inner surface of the transparent substrate 8. A ferroelectric liquid crystal layer 5 is formed between the transparent substrates 2 and 8 and is sealed by a sealing element 6. Respective drivers 3 are connected to the respective scanning electrodes 4 (L_1 to L_{16}) so as to apply a voltage V_c thereto. Respective drivers 10 are connected to the respective signal electrodes 7 so as to apply a voltage V_s thereto.

The operation of the conventional matrix type display panel will be described below.

The portions where the respective electrodes 4 and the respective electrodes 7 are overlapped become picture elements, respectively. In order to bring one of the picture elements into a bright state, it is necessary to apply the voltages V_c and V_s to the corresponding electrodes 4 and 7 so as to satisfy the following equation (1) (or (2)) for τ_0 seconds or more.

$$V_c - V_s \geq V_{th1} \quad (1)$$

$$V_c - V_s \geq -V_{th2} \quad (2)$$

wherein V_{th1} and V_{th2} are threshold voltages of the liquid crystal and τ_0 is a real number larger than zero.

On the other hand, in order to bring one of the picture elements into a dark state, it is necessary to keep another equation (2) (or (1)) for τ_0 seconds or more. Normally, when a direct current voltage is applied to the ferroelectric liquid crystal layer 5, the characteristics of the equations (1) and (2) tend to be shifted. In order to avoid this, before applying a positive voltage V_0 (volts) = $V_c - V_s$ ($V_0 \geq V_{th1}$) to the liquid crystal layer 5 for τ_0 seconds, a negative voltage $-V_0$ (volts) = $V_c - V_s$ ($-V_0 \leq -V_{th2}$) must be applied thereto for τ_0 seconds. On the other hand, before applying the negative voltage $-V_0$ (volts) = $V_c - V_s$ to the liquid crystal layer 5 for τ_0

seconds, the positive voltage $V_0 = V_c - V_s$ must be applied thereto for τ_0 seconds. That is, it takes at least $2\tau_0$ seconds to rewrite the picture elements defined on one scanning electrode 4. The aforementioned switching operation between the positive and negative voltages is referred to the selection of the scanning electrode 4 hereinafter, and the time interval necessary for the aforementioned switching operation is referred to the time interval for selecting the scanning electrode 4. It is to be noted that the voltages V_{th1} , $-V_{th2}$ and τ_0 are determined in accordance with the kinds of the ferroelectric liquid crystal to be used.

In the case that images are displayed on the matrix type display panel shown in Fig. 7, on the assumption that the time for selecting one electrode 4 is $2\tau_0$, it takes $16 \times 2\tau_0$ seconds to select the first electrode L_1 again after selecting the sixteen scanning electrodes L_1 to L_{16} sequentially. A time period needed for selecting all of the sixteen scanning electrodes L_1 to L_{16} sequentially is referred to the frame period T_F hereinafter.

In the case of the matrix type display panel shown in Fig. 7, there are the sixteen scanning electrodes 4, however, in the case that there are M scanning electrodes 4 in a matrix type display panel, the frame period T_F is expressed as follows:

$$T_F = M \times 2\tau_0 \quad (3)$$

wherein M is a positive integer.

When a ferroelectric liquid crystal having the time interval $\tau_0 = 100$ micro seconds is used and the number M of the scanning electrodes 4 is 400, the frame period T_F is 0.08 seconds.

In order to hold one of picture elements in a dark state, it is necessary to apply the negative voltage thereto. However, when the scanning electrode 4 corresponding thereto is selected again after the frame period T_F , it becomes necessary to apply the positive voltage thereto for the time interval τ_0 . Due to this, the picture element is brought into a bright state temporarily. Namely, the picture element becomes the bright state every frame period T_F (= 0.08 seconds).

Thus, when the bright and dark states of the picture element are switched in a period larger than 1/60 seconds, a flicker is caused on the display panel for human's eyes, resulting in that the display panel produces unsightly images.

In order to overcome the aforementioned disadvantages, a matrix type ferroelectric liquid crystal display device further comprising a back light source 11 as shown in Fig. 9 in addition to the structure shown in Fig. 8 is proposed. The back light source 11 is arranged on the outer surface of the polarizer 9, and the light intensity thereof is controlled by a control circuit 12 periodically as shown in (a) of Fig. 10. While the light intensity of the back light source 11 is kept relatively low, the control circuit 12 controls the drivers 3 and 10 so as to select individual scanning electrodes 4 sequentially.

In the aforementioned conventional display device

comprising the back light source 11 and the control circuit 12, when at least one picture element is rewritten, the back light source 11 is turned off so that the whole matrix display panel becomes relatively dark. On the other hand, except for the respective time intervals for rewriting the picture elements, the back light source 11 is turned on.

The aforementioned conventional display device shown in Fig. 9 can prevent the display from flickering certainly. However, since the scanning electrode 4 can not be selected for the time interval when the light intensity of the back light source 11 is kept relatively high, the frame period T_F increases. If the ratio of the time interval for the high light intensity to the time interval for the low light intensity of the back light source 11 is 1 : 1, the frame period T_F becomes twice as long as that in the aforementioned display device not having the back light source. In the case that the time interval t_0 is 100 micro seconds and the number M of the scanning electrodes 4 is 400 as described above, the frame period T_F becomes 0.16 seconds, resulting in that the movement of the images becomes slow in a moving image display.

SUMMARY OF THE INVENTION

An essential object of the present invention is to provide a liquid crystal display device which is able to prevent the display from flicking thereon in a relatively short frame period T_F .

According to the present invention, there is provided a liquid crystal display device comprising: first and second transparent substrates arranged in parallel to each other with a predetermined interval; a plurality of transparent scanning electrodes formed on the inner surface of said first transparent substrate; a plurality of transparent signal electrodes formed on the inner surface of said second transparent substrate so as to cross said transparent scanning electrodes; a liquid crystal layer for displaying picture elements defined at the crossings between said transparent scanning electrodes and said transparent signal electrodes in accordance with a predetermined voltage applied thereto, said liquid crystal layer being arranged between said first transparent substrate and said second transparent substrate; a light source for emitting a light toward said liquid crystal layer; and a light shutter means arranged between said liquid crystal layer and said light source for shading the light incident to the picture elements being rewritten from said light source.

Accordingly, since the light from the light source is not incident to the picture elements being rewritten, any flicker is not caused on the display device. Furthermore, since the picture elements except for the picture elements being rewritten are displayed, the frame period T_F does not increase, resulting in that the quality of images displayed on the liquid crystal display device can be improved.

BRIEF DESCRIPTION OF THE DRAWINGS

These and other objects and features of the present invention will become clear from the following description taken in conjunction with the preferred embodiments thereof with reference to the accompanying drawings, in which:

Fig. 1 is a schematic top plan view showing a liquid crystal display device of the preferred embodiment according to the present invention;

Fig. 2 is a schematic longitudinal cross-sectional view showing the liquid crystal display device shown in Fig. 1;

Fig. 3 is a timing chart showing the operation of the liquid crystal display device shown in Figs. 1 and 2;

Fig. 4 is a schematic top plan view showing a light shutter panel used in the liquid crystal display device shown in Figs. 1 and 2;

Fig. 5 is a schematic longitudinal cross-sectional view showing the light shutter panel shown in Fig. 4;

Fig. 6 is a timing chart showing the operation of the light shutter panel shown in Figs. 4 and 5;

Fig. 7 is a schematic top plan view showing a conventional liquid crystal display device;

Fig. 8 is a schematic longitudinal cross-sectional view showing the liquid crystal display device shown in Fig. 7;

Fig. 9 is a schematic longitudinal cross-sectional view showing a further conventional liquid crystal display device; and

Fig. 10 is a timing chart showing the operation of the liquid crystal display device shown in Fig. 9.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The preferred embodiment according to the present invention will be described below in detail, referring to the attached drawings.

Figs. 1 and 2 show fundamental structures of a two dimensional matrix type liquid crystal display device of the preferred embodiment according to the present invention.

As is apparent from comparison of Fig. 2 with Fig. 9, a light shutter panel 21 is inserted between a matrix display panel 20 and a light source 19. The structures of the light shutter panel 21 and the light source 19 are substantially same as those shown in Fig. 9.

The light shutter panel 21 is comprised of a liquid crystal cell having a sandwich structure of two transparent substrates 13 and 15 of glass and a ferroelectric liquid crystal layer 5a contained therebetween in a sealed state by a sealing member 6a. Between the light shutter panel 21 and the light source 19, a polarizer 16 is inserted so as to satisfy a crossed Nicols relation with the polarizer 9 of the matrix display panel 20.

As shown in Fig. 4, sixteen transparent scanning electrodes 4a (L_{1a} to L_{16a}) are formed on the inner surface of the substrate 13 so as to correspond to the scanning electrodes 4 of the matrix display panel 20, and on the opposed inner surface of the

substrate 15, a transparent rectangular ground electrode 14 is formed so as to cover the whole area of the substrate 15.

These sixteen scanning electrodes 4a (L_{1a} to L_{16a}) are divided into four groups, each being comprised of successive four scanning electrodes L_{1a} to L_{4a} , L_{5a} to L_{8a} , L_{9a} to L_{12a} , L_{13a} to L_{16a} , respectively, and each scanning electrode belonging to one group is connected to a driver 17 so as to be able to apply a voltage V_h commonly. Further, a ground driver 18 is connected to the ground electrode 14 in order to give zero voltage to the same.

Thus, the light shutter panel 21 can shade the light from the light source 19 in the unit of each group when the voltage V_h is applied thereto by the corresponding driver 17 (D_1 , D_2 , D_3 or D_4). In other words, the shutter panel 21 has four shutter elements in this example.

A frequency for driving the drivers D_1 to D_4 is chosen higher than that of the commercial electric power in order to avoid possible flickers. In the preferred embodiment, the light intensity of the light source 19 is kept constant as shown in Fig. 6.

A control circuit 30 applies predetermined voltages to the scanning electrodes 4 (L_1 to L_{16}), the signal electrodes 7 and the scanning electrodes 4a (L_{1a} to L_{16a}) through the drivers 3, the drivers 10 and the drivers 17 (D_1 to D_4), respectively, as described below in detail.

The operation of the matrix type liquid crystal display device of the preferred embodiment will be described below in detail.

Assuming that the matrix display panel 20 has M scanning electrodes 4 to be driven in a frame period T_F and that M scanning electrodes 4 are divided into $2 \times N$ groups from the first to $2N$ -th, each of which is comprised of successive K scanning electrodes 4, wherein M, N and K are positive integers chosen so as to satisfy the following conditions:

$$N > 60 \text{ (Hz)} \times T_F \quad (4)$$

wherein 60 (Hz) is the frequency of the commercial electric power, and

$$K \geq \frac{M}{2N} \quad (5)$$

The first condition is given in order to avoid possible flickers. K is desirably chosen as a minimum integer among integers satisfying the condition (5).

The light shutter panel 21 has the same grouping structure as that of the matrix display panel 20 as far as the scanning electrodes 4a are concerned. Namely, it has $2 \times N$ light shutter elements being comprised of the first to $2N$ -th groups, each group having K successive scanning electrodes 4a.

As stated above, when either one of the first to $2N$ -th light shutter elements is driven by applying the voltage thereto, the light amount incident to the portion of the corresponding group of the scanning electrodes 4 of the matrix display device 20 decrease accordingly.

In this preferred embodiment, if either one of the scanning electrodes 4 belonging to an odd group of the matrix display panel 20 is driven to rewrite, all of the odd shutter elements of the light shutter panel 21 are driven to close. On the contrary, if either one of the scanning electrodes 4 belonging to an even

group is driven to rewrite, all of the even shutter elements of the light shutter panel 21 are driven to close. Respective shutter elements are normally opened except for the case above mentioned.

Thus, the light amount incident to a picture element defined on an arbitrary scanning electrode 4 decreases for a time interval of $T_F/2N$ and increases for the time interval of $T_F/2N$, and this decrease and increase pattern of the light amount is repeated. Namely, the period of the repeated pattern becomes T_F/N , and according to the condition (4), the following inequality is satisfied.

$$\frac{T_F}{N} < \frac{1}{60} \text{ (seconds)} \quad (6)$$

Thus, no flicker is caused in the preferred embodiment.

The operation of the light shutter panel 21 will be described below, referring to a timing chart shown in Fig. 6.

In order to make the light shutter element corresponding to the scanning electrodes L_1 to L_4 open, the control circuit 30 applies a voltage V_h satisfying the following equation (7) (or (8)) to the scanning electrodes L_{1a} to L_{4a} through the driver D_1 for the time interval τ_0 seconds. On the other hand, in order to make the light shutter element corresponding to the scanning electrodes L_1 to L_4 close, the control circuit 30 applies a voltage V_h satisfying the following equation (8) (or (7)) to the scanning electrodes L_{1a} to L_{4a} through the driver D_1 for the time interval τ_0 seconds.

$$V_h \geq V_{th1} \quad (7)$$

$$V_h \geq V_{th2} \quad (8)$$

Namely, when the voltage V_0 (volts) is applied to the scanning electrodes L_{1a} to L_{4a} for the time interval τ_0 seconds after the voltage $-V_0$ (volts) is applied thereto for the time interval τ_0 seconds, the light shutter element corresponding to the scanning electrodes L_1 to L_4 is opened. On the other hand, when the voltage $-V_0$ (volts) is applied to the scanning electrodes L_{1a} to L_{4a} for the time interval τ_0 seconds after the voltage V_0 (volts) is applied thereto for the time interval τ_0 seconds, the light shutter element corresponding to the scanning electrodes L_1 to L_4 is closed.

The experimental result of the liquid crystal display device of the preferred embodiment will be described below.

The drivers D_1 to D_4 are driven so that the light shutter elements are opened and closed as shown in (c) to (f) of Fig. 6, in synchronizing with that the rewritten electrodes 4 of the matrix display panel 20 are selected as shown in (b) of Fig. 6. (c) to (f) of Fig. 3 show the driving voltages applied to the scanning electrodes L_{1a} to L_{4a} , L_{5a} to L_{8a} , L_{9a} to L_{12a} , and L_{13a} to L_{16a} in the above case, and also (g) and (h) of Fig. 3 show the lightness of the picture elements defined on the scanning electrode L_1 when the picture elements are set in the bright and dark states, respectively, in this case.

As is apparent from the above experiment, the lightness of the picture elements changes, mainly, according to the switching period of the light shutter.

Furthermore, a liquid crystal display device having the fundamental structure shown in Figs. 1 and 2 and

having the 400 scanning electrodes 4 was made. When the voltage V_c is applied to the 400 scanning electrodes 4 of the liquid crystal display device, the images with no flicker can be obtained on the liquid crystal display device.

In the present preferred embodiment, the scanning electrodes 4 are selected sequentially in the order from the scanning electrode L_1 to L_{16} as shown in (b) of Fig. 6. However, there is no problem even though the light shutter element corresponding to the scanning electrode 4 being far away from the selected scanning electrode 4 is opened.

In the present preferred embodiment, the ferroelectric liquid crystal panel 21 is used as the light shutter panel. However, a lighting apparatus having the same effect as that of the ferroelectric liquid crystal panel, such as an electroluminescence display, a vacuum fluorescent display, a plasma display panel, and a twisted nematic liquid crystal display, respectively comprising strip shaped picture elements arranged along the scanning electrodes 4 of the matrix display panel 20, can be used as the light shutter panel.

In the present preferred embodiment, as shown in Fig. 6, when the light shutter element corresponding to the scanning electrodes L_1 to L_4 are closed, the light shutter element corresponding to the scanning electrodes L_5 to L_8 are opened and the light shutter element corresponding to the scanning electrodes L_9 to L_{12} are closed. Namely, there are provided the opened light shutter element and the shut light shutter element alternately at intervals of the aforementioned scanning electrode group. In this case, the flicker can be preferably suppressed. However, the present invention is not limited to this, the light shutter elements corresponding to the scanning electrodes 4 of the picture elements being rewritten may be closed, and the other light shutter elements may be opened, resulting in that the flicker can be suppressed sufficiently.

In the present preferred embodiment, the scanning electrodes 4 corresponds to one light shutter element in the unit of the electrode group, because it is difficult to make the scanning electrodes 4 correspond to the light shutter elements one by one precisely when the matrix display panel 20 is overlapped on the light shutter panel 21. When the panels 20 and 21 are overlapped with a high precision so that the scanning electrodes 4 correspond to the light shutter elements one by one, respectively, the opening and closing operation of every one light shutter element corresponding to the selected one electrode 4 may be controlled.

As shown in Fig. 6, the opening time interval OP of the light shutter elements is smaller than a predetermined time interval. For example, when the light is projected to the scanning electrodes L_5 to L_8 , the light shutter element corresponding to the scanning electrodes L_5 and L_8 are closed and the light shutter element corresponding to the scanning electrodes L_6 and L_7 is opened. Because it is taken into consideration that the light transmitting through the opened light shutter element spreads out when there is a gap between the matrix display panel 20 and the light shutter panel 21. Therefore, in the case

that the liquid crystal display device has a structure where the light transmitting through the opened light shutter element does not spread out, the opening time interval OP may not be smaller than the predetermined time interval.

It is understood that various other modifications will be apparent to and can be readily made by those skilled in the art without departing from the scope and spirit of the present invention. Accordingly, it is not intended that the scope of the claims appended hereto be limited to the description as set forth herein, but rather that the claims be construed as encompassing all the features of patentable novelty that reside in the present invention, including all features that would be treated as equivalents thereof by those skilled in the art to which the present invention pertains.

Claims

1. A liquid crystal display device comprising: first and second transparent substrates arranged in parallel to each other with a predetermined interval;
a plurality of transparent scanning electrodes formed on the inner surface of said first transparent substrate;
a plurality of transparent signal electrodes formed on the inner surface of said second transparent substrate so as to cross said transparent scanning electrodes;
a liquid crystal layer for displaying picture elements defined at the crossings between said transparent scanning electrodes and said transparent signal electrodes in accordance with a predetermined voltage applied thereto, said liquid crystal layer being arranged between said first transparent substrate and said second transparent substrate;
a light source for emitting a light toward said liquid crystal layer; and
a light shutter means arranged between said liquid crystal layer and said light source for shading the light incident to the picture elements being rewritten from said light source.

2. A liquid crystal display device as claimed in claim 1, wherein said transparent scanning electrodes are orthogonal to said transparent signal electrodes, and the picture elements of said liquid crystal layer are formed in a matrix shape.

3. A liquid crystal display device as claimed in claim 1, wherein said light shutter means comprises:

- third and fourth transparent substrates arranged in parallel to each other with a predetermined interval;
a plurality of further transparent scanning electrodes formed on the inner surface of said third transparent substrate so as to face said respective transparent scanning electrodes;
a ground electrode formed on the inner surface of said fourth transparent substrate so as to face the whole area of said further transparent

scanning electrodes; and
a further liquid crystal layer for forming light shutter elements defined on said further transparent scanning electrodes in accordance with a predetermined voltage applied thereto, said further liquid crystal layer being arranged between said third transparent substrate and said fourth transparent substrate.

4. A liquid crystal display device as claimed in claim 1, wherein said light shutter means is an electroluminescence display, a vacuum fluorescent display, a plasma display, or a twisted

nematic liquid crystal display.

5. A liquid crystal matrix display comprising an array of selectively controllable liquid crystal display elements, a light source for emitting light to illuminate said display elements, and light shutter means between the array and the light source, said light shutter means having individually controllable shutter portions for shading corresponding portions of the array from said light source.

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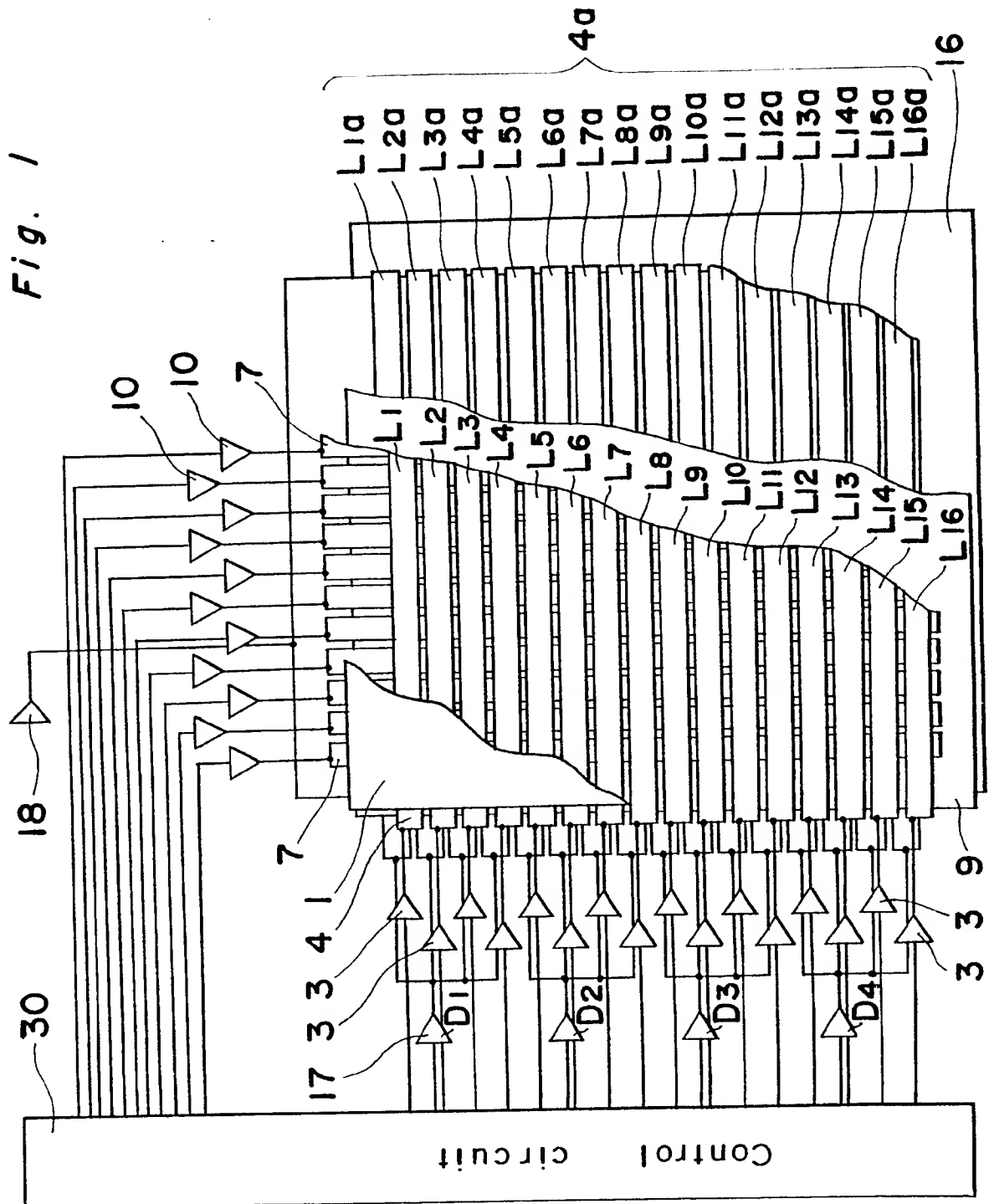


Fig. 2

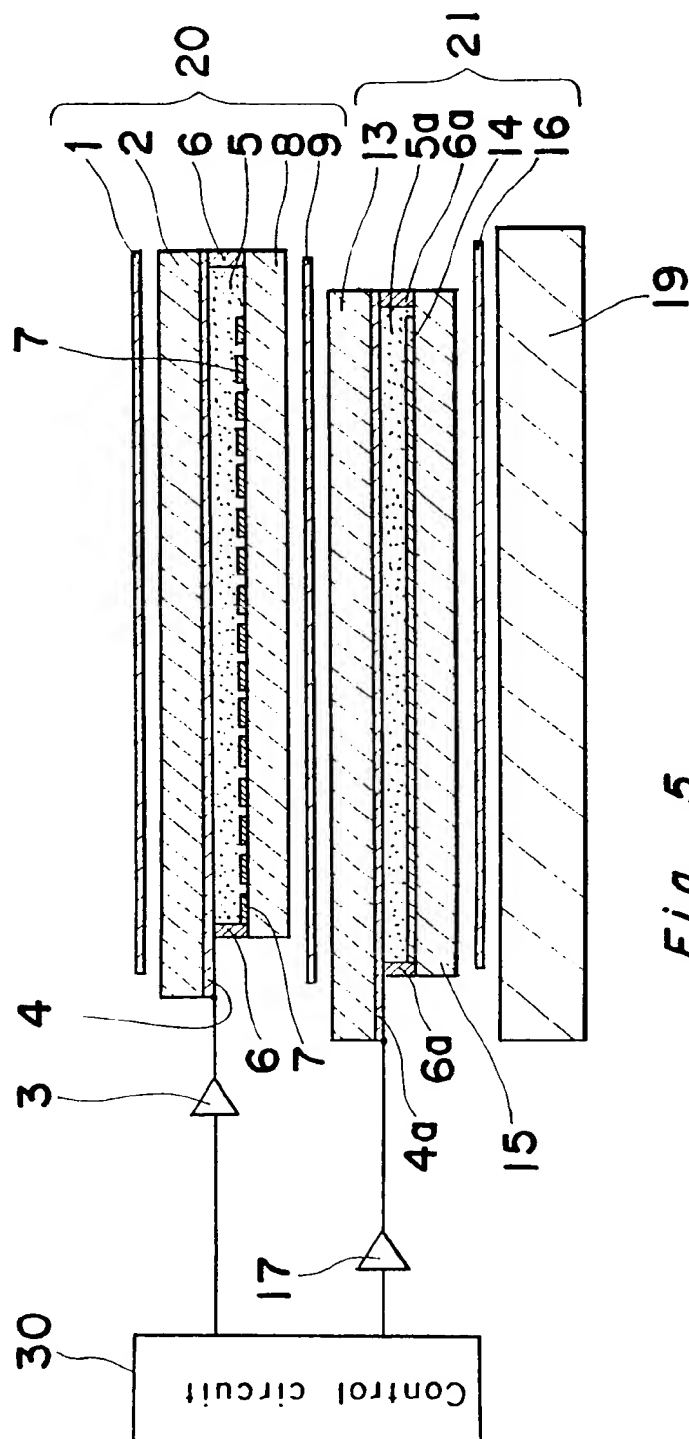


Fig. 5

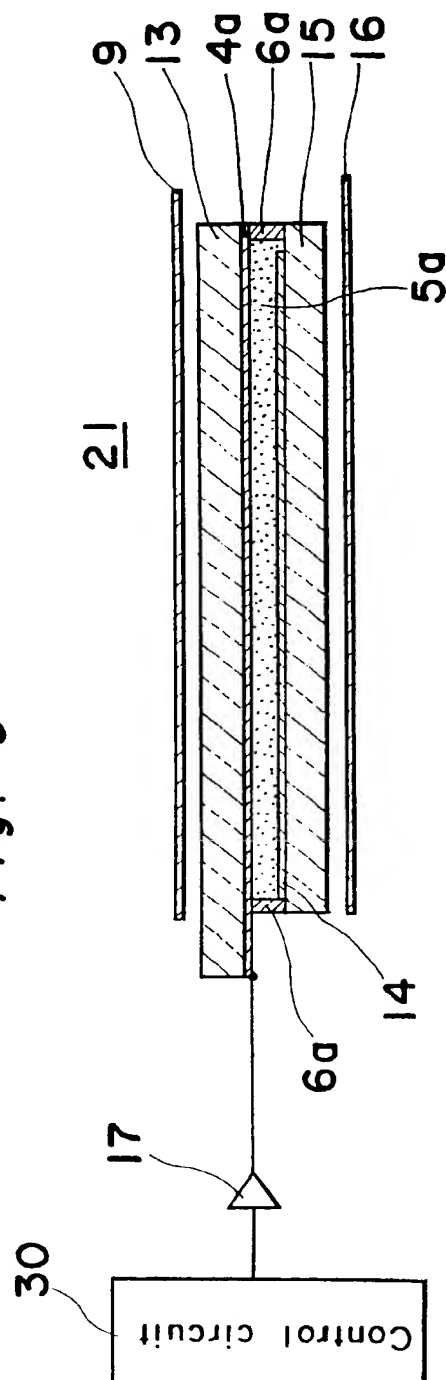


Fig. 3

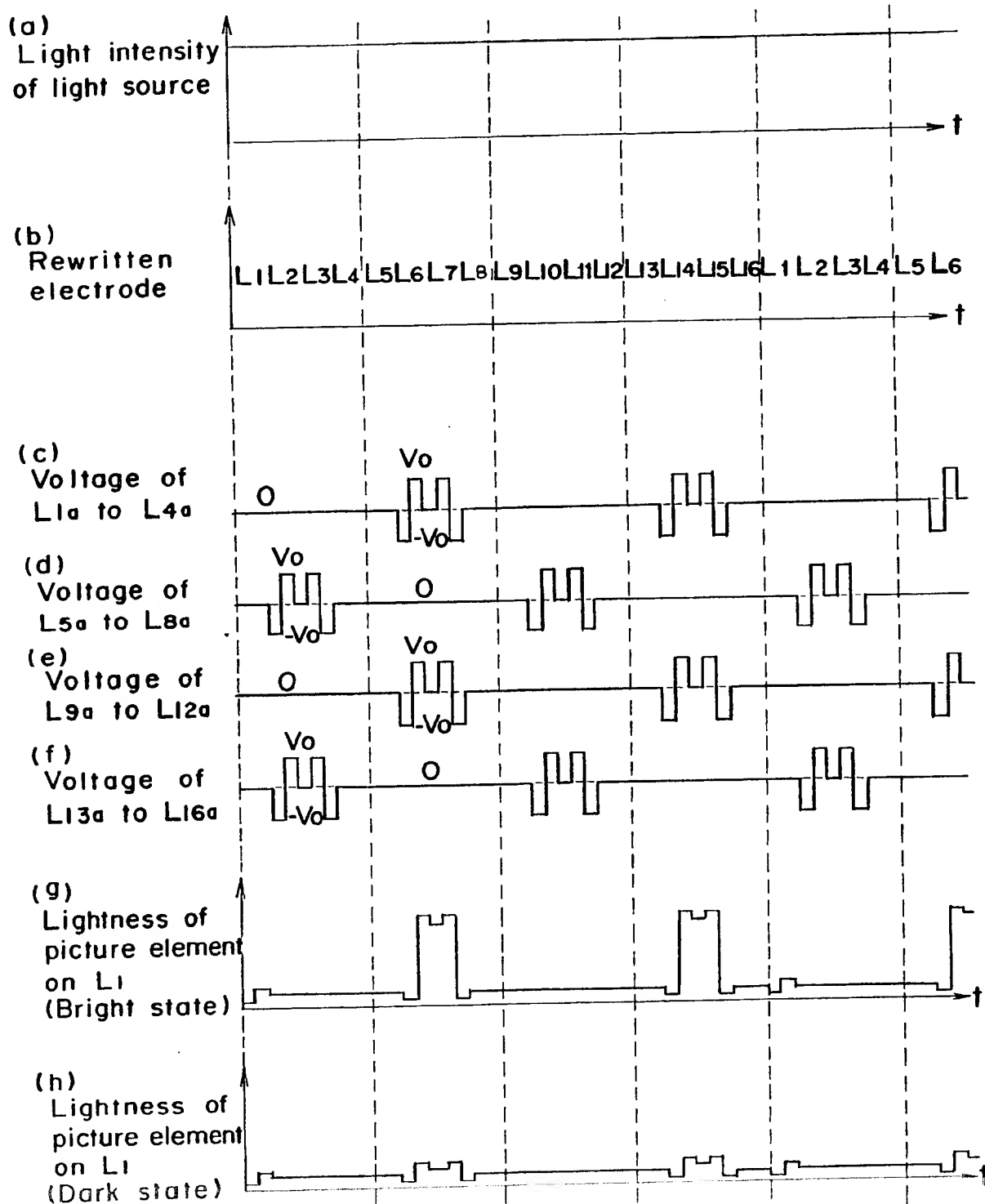


Fig. 4

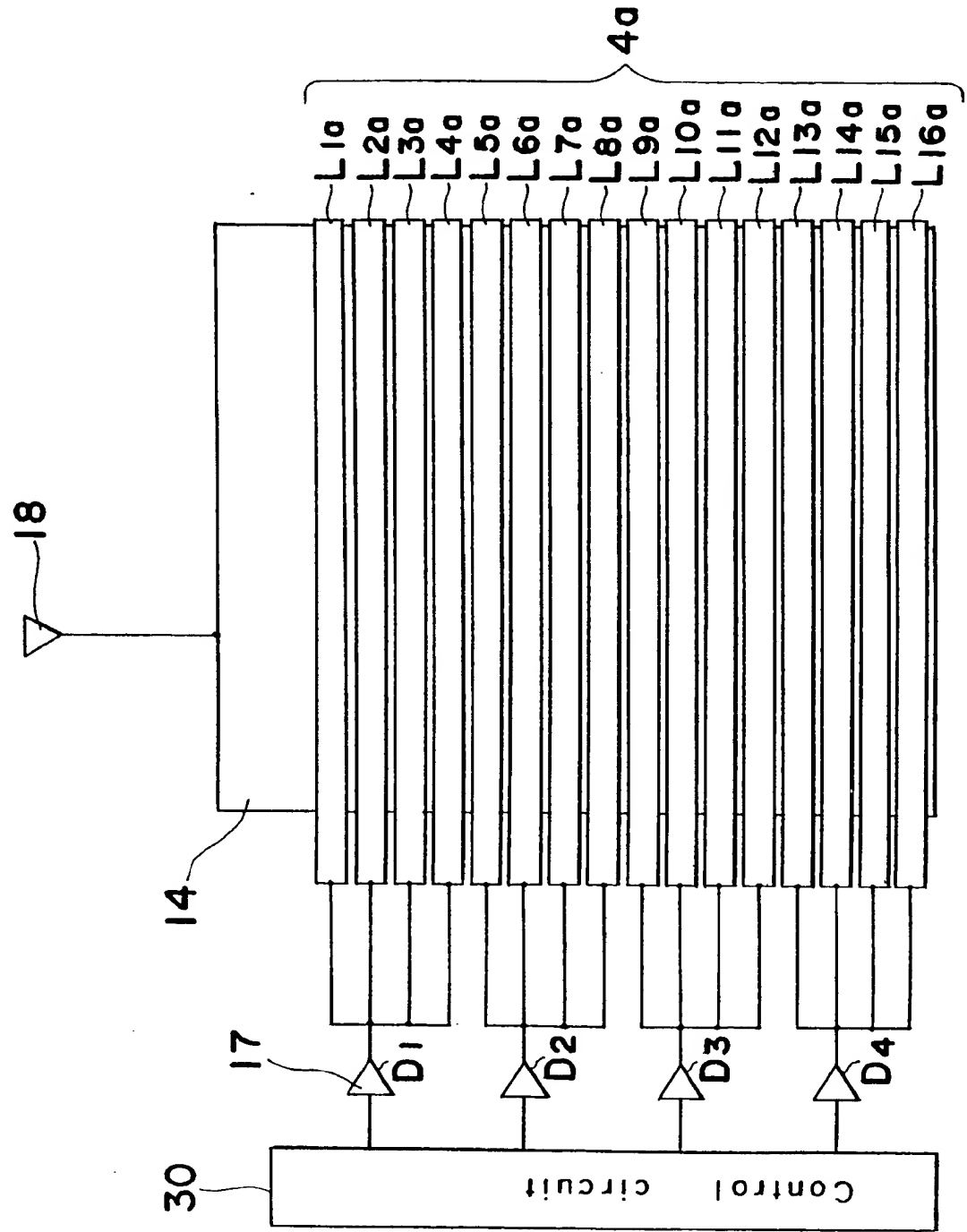


Fig. 6

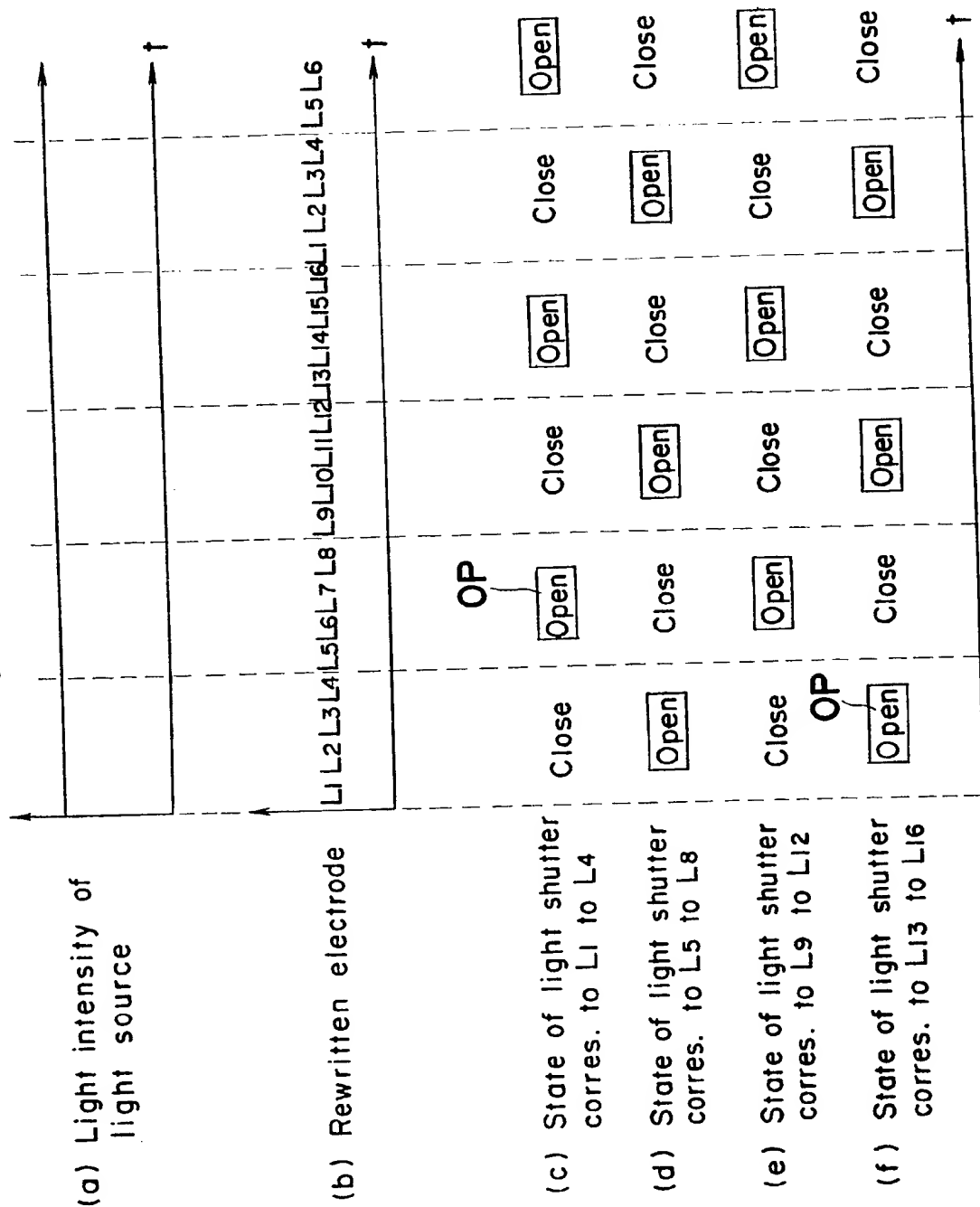


Fig. 7 PRIOR ART

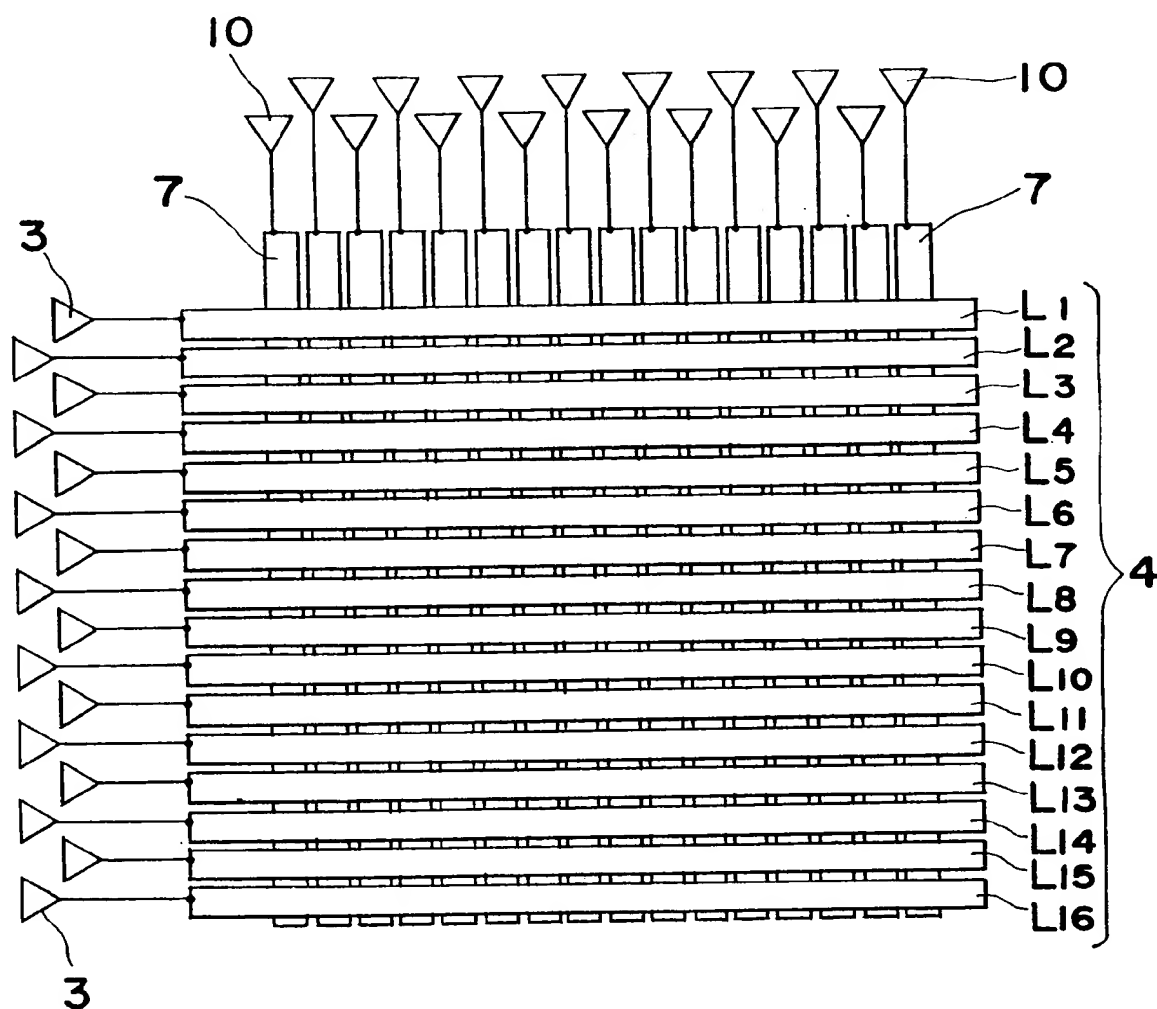


Fig. 8 PRIOR ART

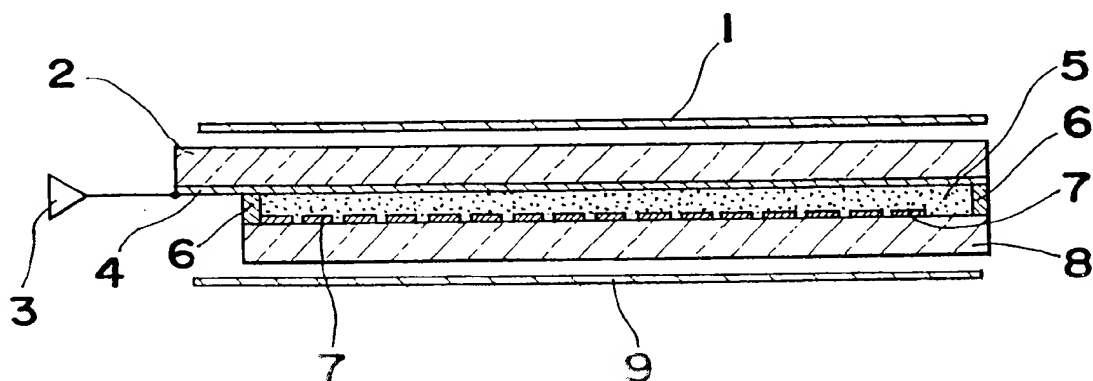


Fig. 9 PRIOR ART

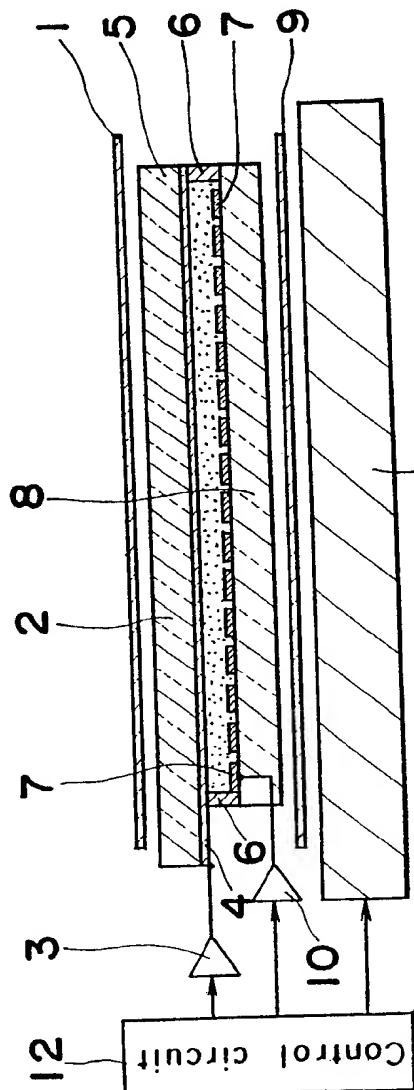
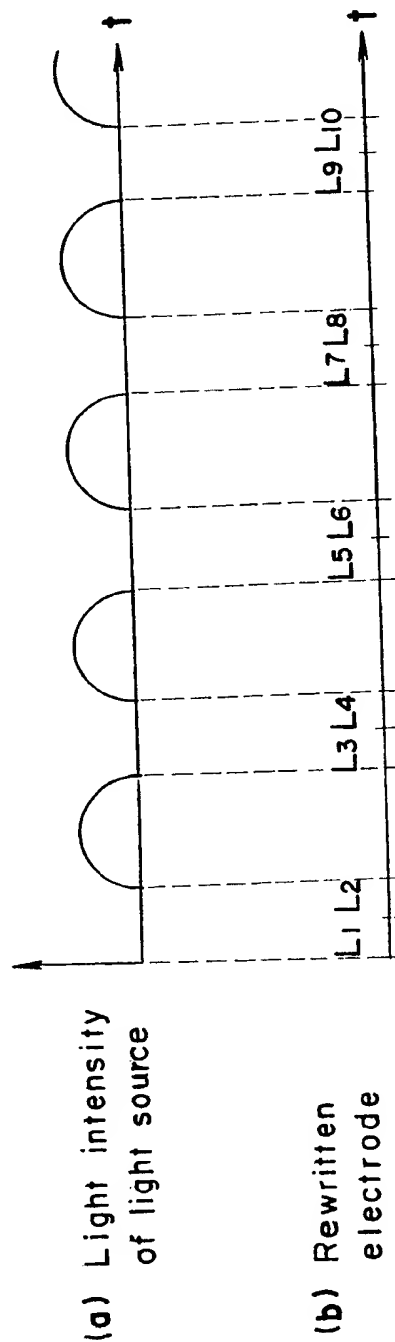
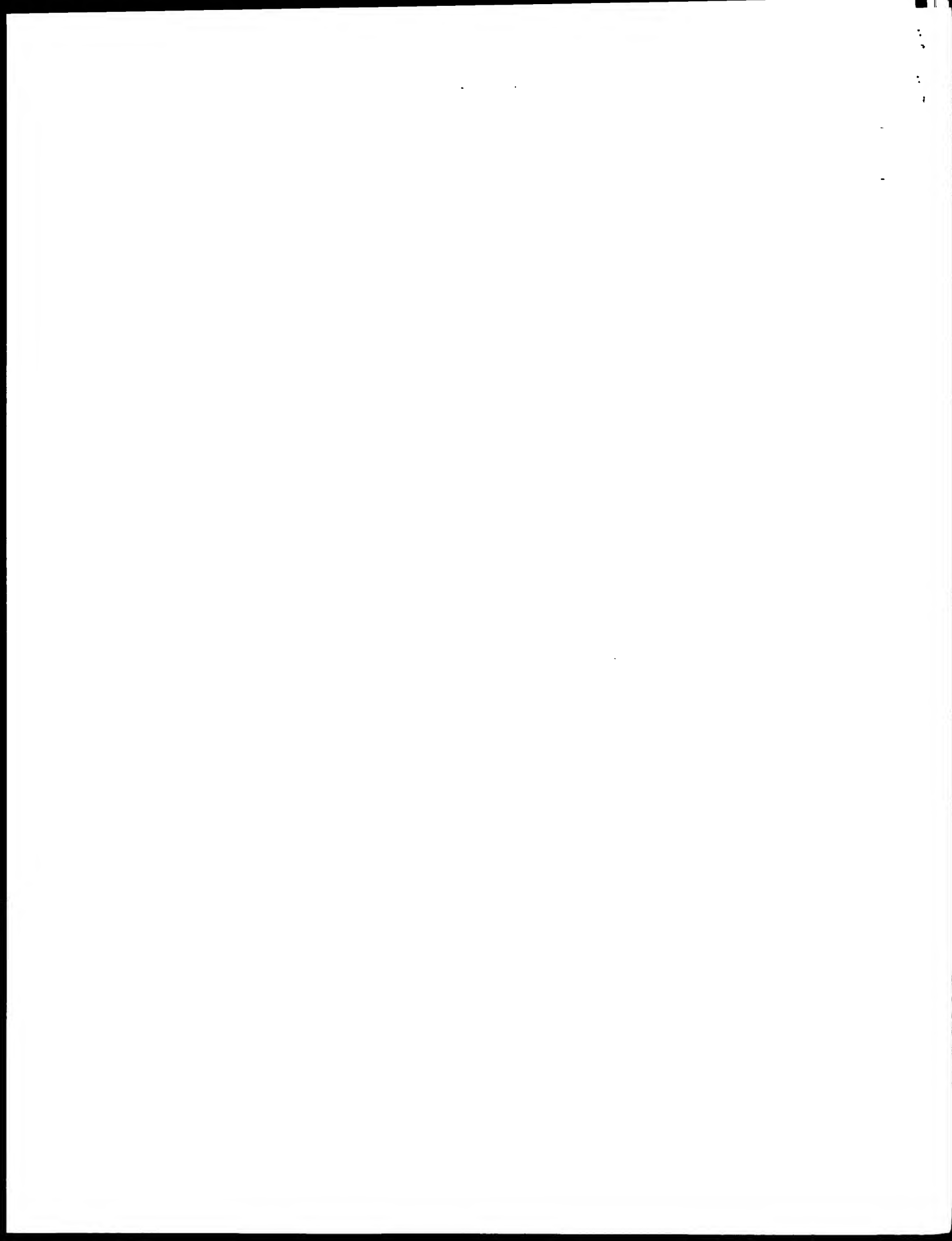


Fig. 10 PRIOR ART







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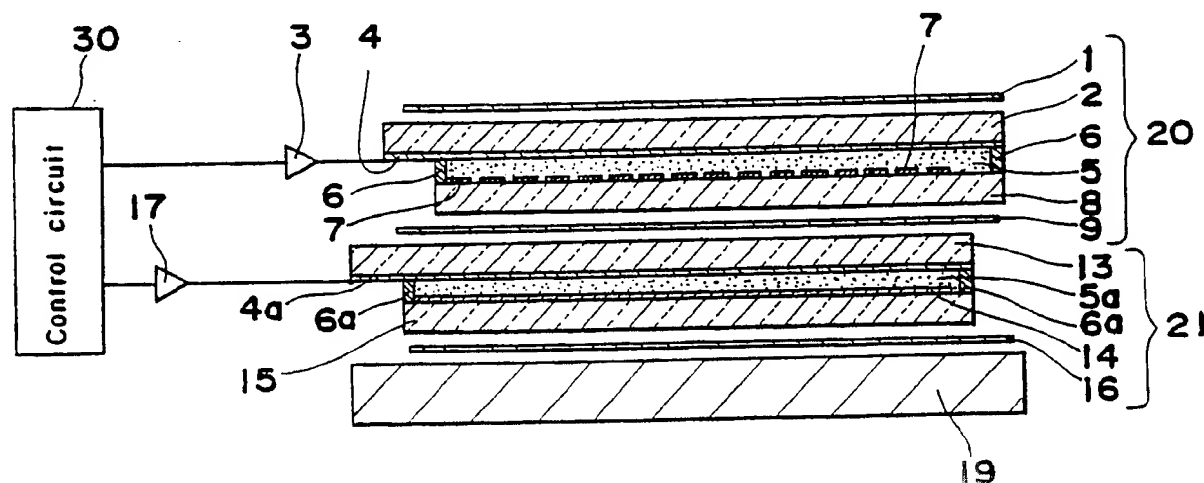
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(54) **Liquid crystal display device.**

(57) A liquid crystal display device comprising a liquid crystal display panel (20) for displaying picture elements in accordance with a predetermined voltage applied thereto is disclosed. In the liquid crystal display device, a light shutter (21) is arranged be-

tween the liquid crystal display panel (20) and a light source (19), and the light shutter (21) shades the light incident to the picture elements being rewritten from the light source.

Fig. 2



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European Patent
Office

EUROPEAN SEARCH REPORT

Application Number

DOCUMENTS CONSIDERED TO BE RELEVANT			EP 89301090.0
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl. 4)
A	<u>FR - A1 - 2 578 994</u> (COMMISARIAT) * Page 4, line 10 - page 5, line 16; page 7, line 19 - page 16, line 10 * --	1-3,5	G 02 F 1/133 G 09 F 9/35 G 09 G 3/36
A	<u>FR - A1 - 2 597 641</u> (THOMSON-CSF) * Fig. 1-4,6; page 7, lines 1-28 * --	1,3-5	
A	<u>US - A - 4 099 855</u> (WISBEY et al.) * Fig. 2; column 2, line 21 - column 4, line 9 * --	1-3,5	
A	<u>CH - A5 - 578 187</u> (MERZ) * Fig. 3 * -----	1,3,5	
			TECHNICAL FIELDS SEARCHED (Int. Cl. 4)
			G 02 F G 09 F G 09 G
The present search report has been drawn up for all claims			
Place of search VIENNA		Date of completion of the search 06-11-1990	Examiner GRONAU
CATEGORY OF CITED DOCUMENTS			
X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document I : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document			

EPD FORM 150 (3.82) (P0601)